

Enrollment No: _____

Exam Seat No: _____

C. U. SHAH UNIVERSITY

Winter Examination-2022

Subject Name : Computer Basics and Organization

Subject Code : 4CS01ICO2

Branch: B.Sc.I.T.

Semester: 1

Date: 05/01/2023

Time: 11:00 To 02:00

Marks: 70

Instructions:

- (1) Use of Programmable calculator & any other electronic instrument is prohibited.
- (2) Instructions written on main answer book are strictly to be obeyed.
- (3) Draw neat diagrams and figures (if necessary) at right places.
- (4) Assume suitable data if needed.

Q-1 Attempt the following questions: (14)

- a) Computer address bus is _____.
 - A. Unidirectional
 - B. Bidirectional
 - C. Multidirectional
 - D. Allowed Logic Unit
- b) Which of the following circuit used to store one bit of data?
 - A. Decoder
 - B. Flip flop
 - C. Encoder
 - D. None of these
- c) The collection of 8 bit is called as?
 - A. Nibble
 - B. Word
 - C. Byte
 - D. Record
- d) In which of the following form computer stores it data in memory?
 - A. Decimal
 - B. Binary
 - C. Hexa Decimal
 - D. Octal
- e) The status bit is also called as?
 - A. Signed bit
 - B. Unsigned bit
 - C. Flag bit
 - D. None of these
- f) The gate which is used to reverse the output obtained is _____.
 - A. And
 - B. XOR
 - C. Not
 - D. None of these
- g) Which of the following topology is used in Ethernet?
 - A. Bus
 - B. Ring
 - C. Star
 - D. Mesh
- h) Which of the following is a combinational logic circuit that has $2n$ input lines and a single output line?.
 - A. Multiplexer
 - B. Demultiplexer
 - C. Decoder
 - D. Encoder



b) What is the role of full adder and half adder? Describe both in detail (7)

Q-8

Attempt all questions:

a) What is Decimal and Binary number system? Discuss conversion of both system with example (7)

b) Discuss ASCII and EDBDIC Code in detail (7)

